

ABSTRACT

LIQUID CRYSTAL DISPLAY AND METHOD OF MANUFACTURE

5 A transistor substrate for a liquid crystal display comprises an array of insulated-gate staggered TFTs and a capacitor (36) associated with each transistor. The gate insulator (400,420) comprises a first inorganic layer (400) and a second, polymer or spin-on glass layer (420), of which layers only the polymer or spin-on glass layer (420) extends to the capacitor (36) to define the
10 capacitor dielectric.

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